

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Atty. Docket

NORBERT FELBER ET AL

CH 000024

Serial No.

Group Art Unit

Filed: CONCURRENTLY

Ex.

Title: METHOD FOR TESTING INTEGRATED CIRCUITS

Commissioner for Patents  
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Prior to calculation of the filing fee and examination, please amend the above-identified application as follows:

IN THE CLAIMS

Please amend the claims as follows:

5. (amended) The method of claim 1, wherein means (14, 15; 24, 25; 34, 35) are used to electrically separate said circuit cell into the first (12; 22; 32) of said two independent cell networks and the second (13; 23; 33) of said two independent cell networks.
6. (amended) The method of claim 1, wherein load means (16, 17; 26, 27; 36, 37) are used which act as loads for said two